

F I G.

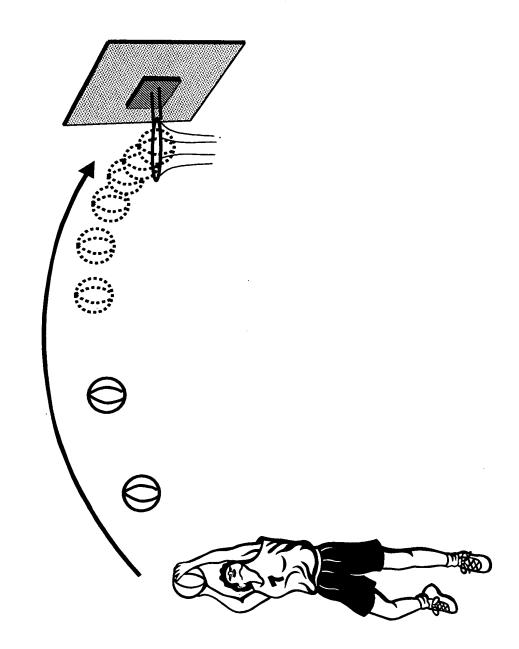
F1G. 2

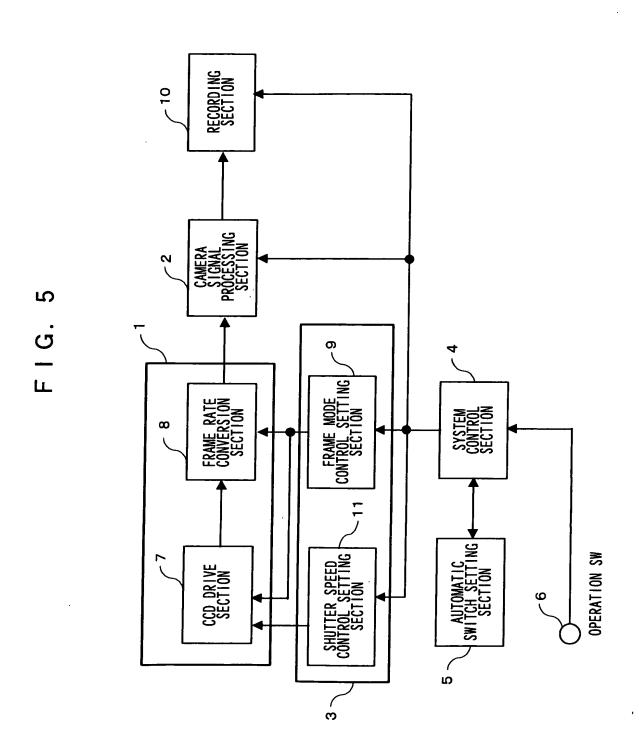
SYSTEM CONTROL SECTION SET TIME (SECOND) FRAME MODE SETTING ហ FRAME RATE (Hz) 24 40 48 ORDER 8 ന വ 9 ω တ 12 13 14

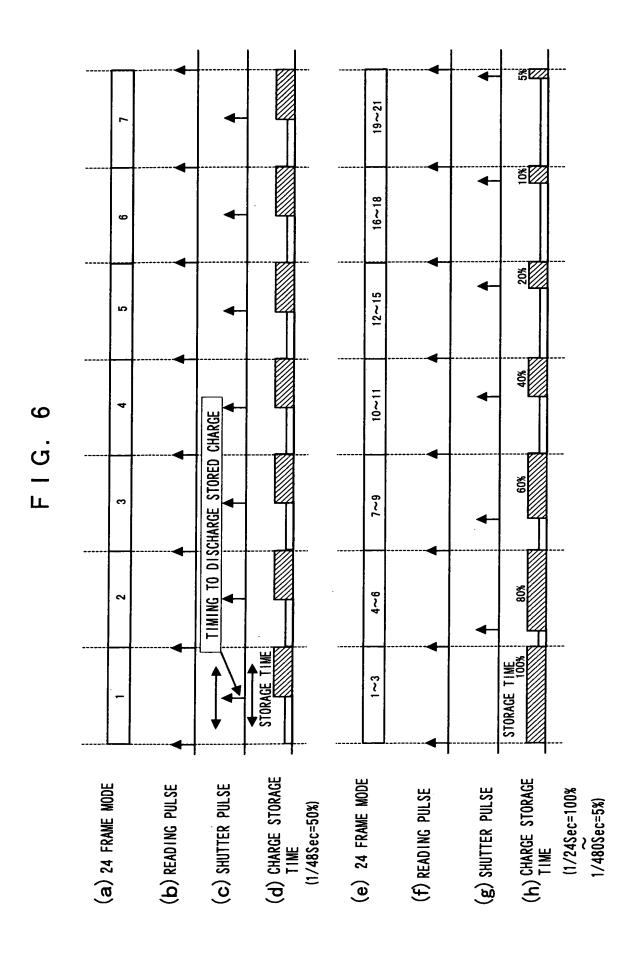
F I G. 3

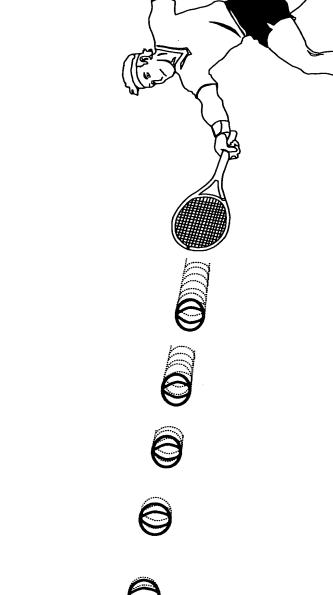
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F I G. 4



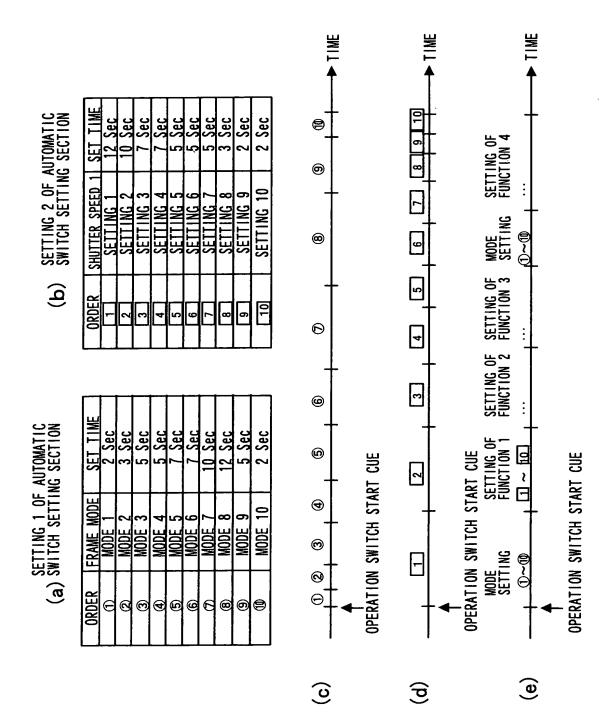


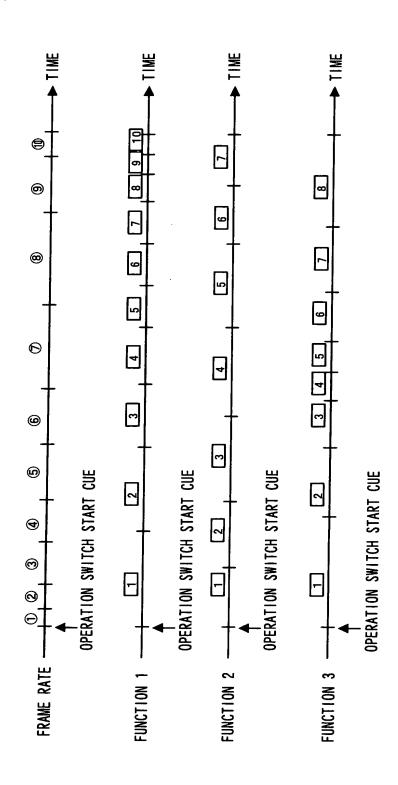




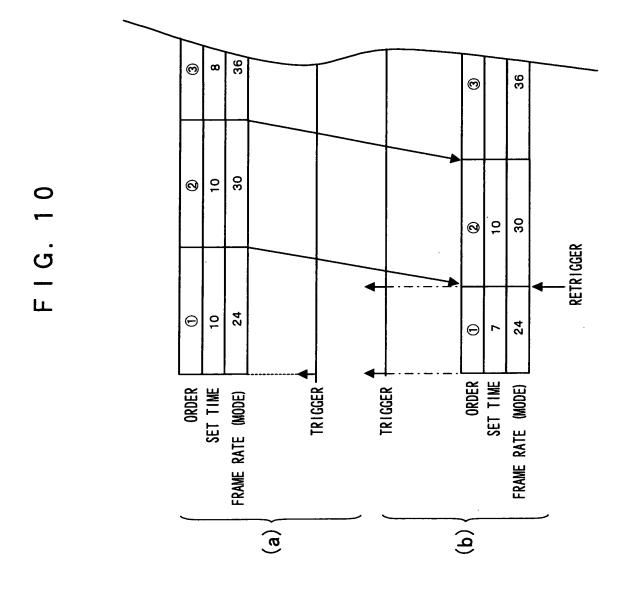
П G.

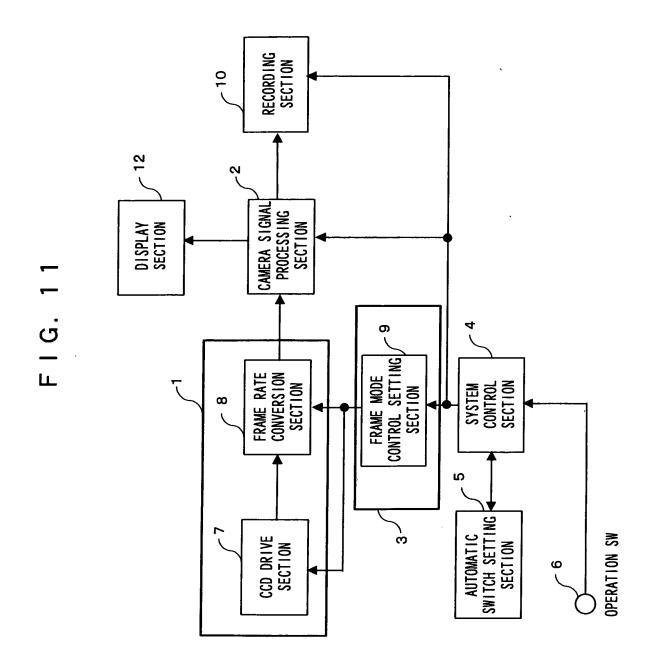
F | G.

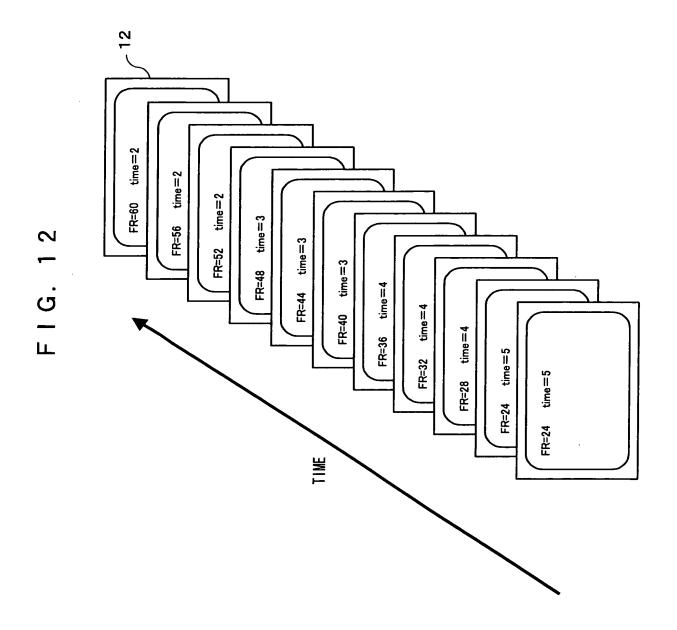




F I G. 9

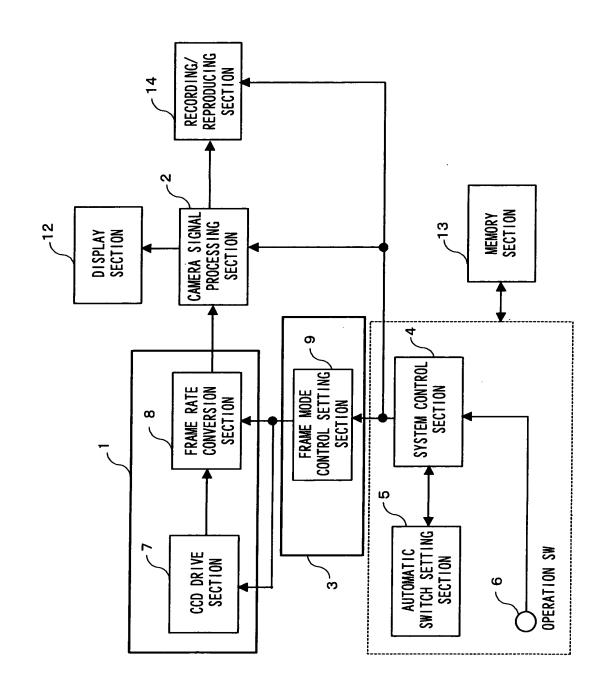


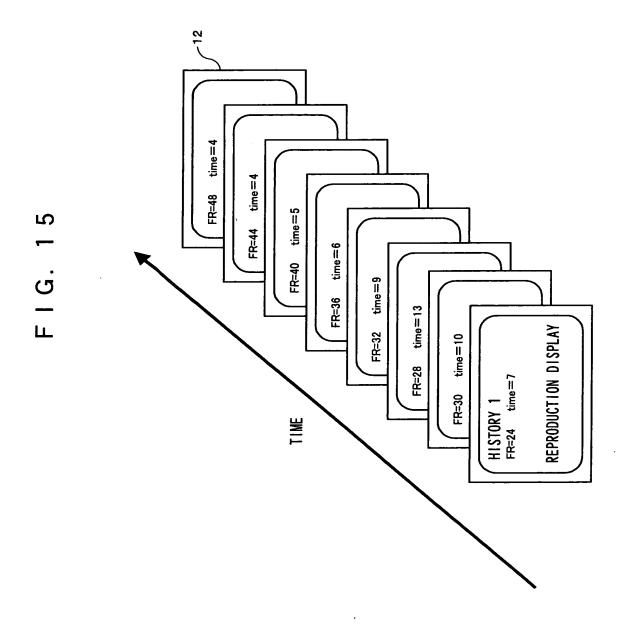


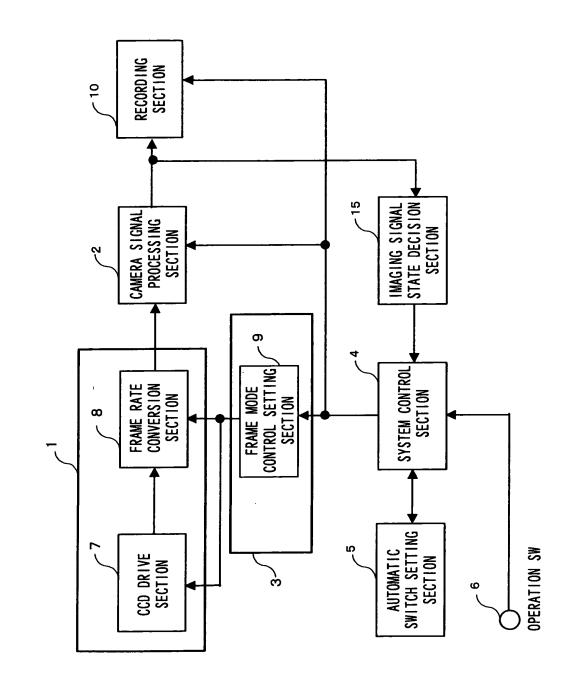


RECORDING SECTION 9 CAMERA SIGNAL PROCESSING SECTION MEMORY Section FIG. 13 FRAME MODE CONTROL SETTING SECTION SYSTEM CONTROL SECTION FRAME RATE CONVERSION SECTION ω Ŋ SWITCH SETTING SECTION CCD DRIVE SECTION OPERATION SW 9

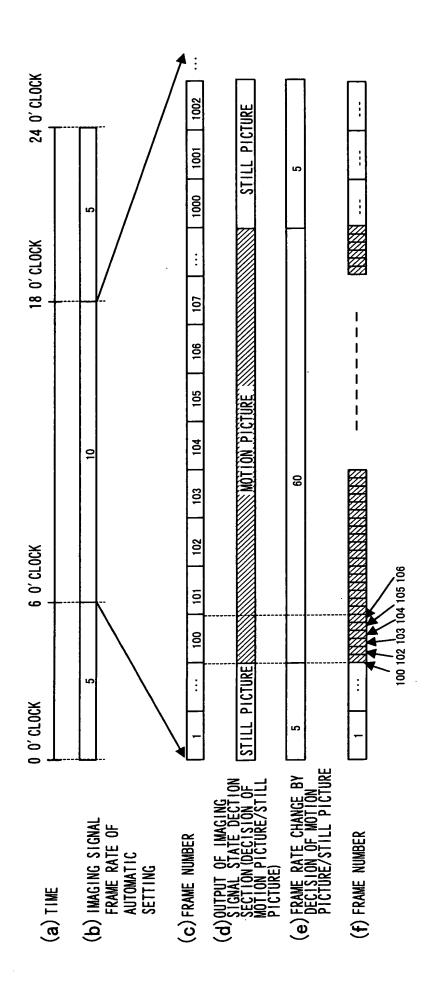


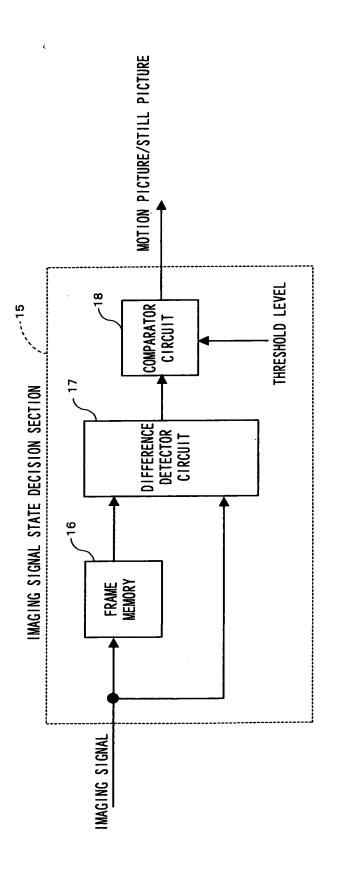






F1G. 16



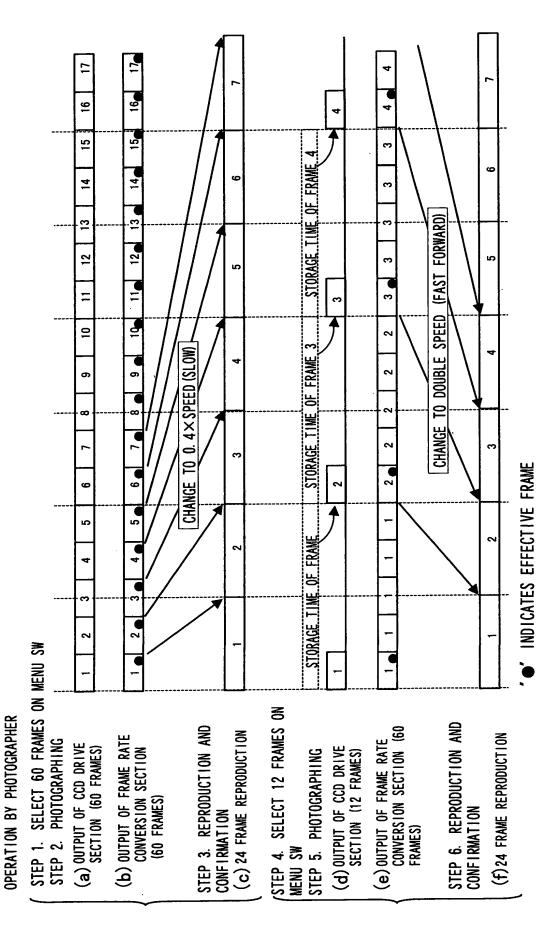


RECORDING SECTION CAMERA SIGNAL PROCESSING SECTION ART PRIOR တ FRAME MODE CONTROL SETTING SECTION SYSTEM CONTROL SECTION FRAME RATE CONVERSION SECTION ∞ OPERATION SW CCD DRIVE SECTION ω /

FIG. 19

F1G. 20

PRIOR ART



12/12